



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/921,518  
Filing Date . . . . . August 1, 2001  
Inventor . . . . . John T. Moore  
Assignee . . . . . Micron Technology, Inc.  
Group Art Unit . . . . . 2818  
Examiner . . . . . Phuc T. Dang  
Attorney's Docket No. . . . . MI22-1669  
Title: Method of Forming Integrated Circuitry, Method of Forming Memory Circuitry  
and Method of Forming Non-Volatile Random Access Memory Circuitry

**RESPONSE TO JUNE 26, 2002 NOTICE OF ALLOWANCE**  
**PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING**

9/ Pre-  
Response  
G. Stork  
8/22/02  
RECEIVED  
AUG - 7 2002  
TCB RD MAIL ROOM

To: Box RCE  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
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Responsive to the Notice of Allowance dated June 26, 2002, Applicant  
remarks as follows:

**REMARKS**

Claims 1-46 remain in the application for consideration.

A Notice of Allowance (NOA) dated June 26, 2002 has been received  
by the Applicant. However, Applicant properly filed a Supplemental IDS on  
June 26, 2002, with an accompanying PTO-1449 and cited art. The art listed